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<b>FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/813,687
				Filing Date	March 31, 2004
				First Named Inventor	Patricius Aloysius Jacobus TINNEMANS
				Art Unit	2851
				Examiner Name	Nguyen, Hung
Sheet	1	of	1	Attorney Docket Number	1857.7580002

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
HN	NPL1	Peter van Zant, Microchip Fabrication: A Practical Guide to Semiconductor Processing, McGraw Hill Publishing Co., Third Edition, 1997, ISBN 0-07-067250-4.	
	NPL2		
	NPL3		
	NPL4		
	NPL5		
	NPL6		
	NPL7		
	NPL8		
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Examiner Signature	/H.Nguyen/	Date Considered	8/20/08
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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